



PTO/SB/21 (09-04)

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**TRANSMITTAL  
FORM**

(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission

9

Application Number

10/672,125

Filing Date

September 26, 2003

First Named Inventor

Dillon, Michael N.

Art Unit

2838

Examiner Name

Nguyen, Matthew Van

Attorney Docket Number

03-0833

**ENCLOSURES (Check all that apply)**

Fee Transmittal Form



Fee Attached



Amendment/Reply



After Final



Affidavits/declaration(s)



Extension of Time Request



Express Abandonment Request



Information Disclosure Statement



Certified Copy of Priority Document(s)

Reply to Missing Parts/  
Incomplete ApplicationReply to Missing Parts  
under 37 CFR 1.52 or 1.53

Drawing(s)



Licensing-related Papers



Petition

Petition to Convert to a  
Provisional Application

Power of Attorney, Revocation



Change of Correspondence Address



Terminal Disclaimer



Request for Refund



CD, Number of CD(s) \_\_\_\_\_

☐ Landscape Table on CD

Remarks

- Issue Fee Payment



After Allowance Communication to TC

Appeal Communication to Board  
of Appeals and InterferencesAppeal Communication to TC  
(Appeal Notice, Brief, Reply Brief)

Proprietary Information



Status Letter

Other Enclosure(s) (please identify  
below):1. Power of Attorney and Correspondence  
Address Indication Form, PTO/SB/812. Statement Under 37 CFR 3.73(b),  
PTO/SB/96**SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT**

Firm Name

LSI Logic Corporation

Signature

Printed name

Timothy R. Croll

Date

7/24/05

Reg. No.

36,771

**CERTIFICATE OF TRANSMISSION/MAILING**

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below:

Signature

Typed or printed name

Mark Salvatore

Date

7-11-05

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# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**In re Application of :**

Michael Dillon  
Bret Oeltjen



**Serial No. :** 10/672,125

**Filed :** September 26, 2003

**For :** Semiconductor Integrated Circuit  
Having Voltage-Down Circuit  
Regulator and Charge Sharing


**Group Art Unit :** 2838

**Examiner :** Nguyen, M.

**Atty Docket :** L13.12-0243 / 03-0833

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Mark Salvatore

July 11, 2005   
Date Signature

## SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85

**Official Draftsman**

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

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Date: July 05

Respectfully submitted,



Timothy Croll

Reg. No. 36,771